

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <u>1503</u> Q.A. <u>AA</u>	PATENT DATE
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APPLICATION NO. 09/751844	CONT/PRIOR F	CLASS 36 174	SUBCLASS 556	ART UNIT 2841 2829	EXAMINER NORZIS
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APPLICANTS

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Circuit board and method of manufacturing therefor

TITLEPTO-2040
12/99

ISSUING CLASSIFICATION								
ORIGINAL				CROSS REFERENCE(S)				
CLASS		SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION								

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig. 4	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____		ISSUE FEE	
	_____ (Primary Examiner) (Date)		Amount Due	Date Paid
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